

INTER - UNIVERSITY ACCELERATOR CENTRE

(An Autonomous Centre of UGC)
Aruna Asaf Ali Marg, New Delhi - 110067

NOTICE INVITING E – TENDER

Tender No: IUAC/NIT/50/PS/2019-20

Dated: 22/01/2020

Inter - University Accelerator Centre (IUAC) invites online bids through e-procurement Portal under two bid system, viz., Technical and Financial bids, from authorized distributors for the **“Supply of NXP Semiconductors make RF power LDMOS transistors as per the Annexure-A ”**.

Tender Documents may be downloaded from Central Public Procurement (CPP) Portal <https://eprocure.gov.in/eprocure/app>.

Aspiring Bidders who have not enrolled / registered in e-procurement portal should enroll / register before participating through the website <https://eprocure.gov.in/eprocure/app>.

Bids should be submitted online only at website: <https://eprocure.gov.in/eprocure/app>. Tenderers / Contractors / Bidders are advised to follow the instructions provided in the e-procurement portal. Bid documents may be scanned with 100 dpi with black and white option which helps in reducing size of the scanned documents.

Only bids received through e-procurement portal will be considered for opening. Bids not covering full scope of work/supply of the products/goods will be rejected and only complete bids will be considered. IUAC reserves the right to accept / reject any / all tenders in part / full without assigning any reasons whatsoever, and the decision of IUAC in this regard will be binding on all the bidders.

Bidders have to select the payment option as **“offline”** to pay the tender fee and EMD as applicable. The Earnest Money Deposit (EMD) and tender cost shall be in the form of demand draft issued in favor of **“Inter University Accelerator Centre, New Delhi”** and it should be deposited at IUAC before the bid opening. Bidders registered with MSME / NSIC and foreign bidders quoting directly are exempted from payments of EMD and tender fee. Copy of valid registration certificate should be uploaded in the portal.

Bidders are advised to note that they should necessarily submit their financial bids in the standard BOQ format provided and no other format is acceptable. BOQ format is to be downloaded and to be filled and uploaded without modification. If the BOQ file is found modified by the bidder, the bid will be rejected.

Any Corrigendum / Amendments in respect of above tender shall be issued on IUAC website www.iuac.res.in and the website <https://eprocure.gov.in> only. Bidders should take into account any corrigendum published on the tender document before submitting their bids.

E - TENDER DOCUMENT

Name of Work / Supply	Supply of NXP Semiconductors make RF power LDMOS transistors as per the Annexure-A
Tender Number	IUAC/NIT/50/PS/2019-20
Tender Value (Estimate only)	Rs : 9,00,000/- (Nine lakhs only)
Earnest Money Deposit	Rs : 18,000/- (Eighteen thousand only)
Cost of tender	Rs. 590/- (Five hundred ninety only)
Bid Submission End Date and Time	20/02/2020 at 3:00 P.M.
Technical Bid Opening Date (Part - A)	21/02/2020 at 3:30 P.M.
Price Bid Opening Date (Part - B)	To be intimated later on to the technically qualified bidders.
Address for contact	M. B. Joseph, Administrative Officer (S&P), Inter University Accelerator Centre, Aruna Asaf Ali Marg, New Delhi - 110067 E-mail: joseph@iuac.res.in Phone: 011-24126018 & 24126022

General Terms and Conditions of the Tender:

1) Submission of Tender:

Tenders should be uploaded on CPP Portal in two parts, i.e., Technical Bid (Part - A) and Price Bid / BOQ (Part - B).

2) Technical Bid (Part-A):

In this bid, the bidder should upload the following documents required for technical qualifications. No deviations in respect of NIT conditions are acceptable.

- a) A promising letter on company letter head duly signed and stamped, the **Certificate of Compliance** (CoC) will have to be provided by vendors along with the materials to ensure the genuine material is being supplied.
- b) RF power LDMOS transistors package **NI-1230H-4S** in its original package only will be accepted. No rejected or repackaged items will be accepted under this purchase.
- c) Catalogues or data sheet and other technical specifications of the products indicating its ability to meet the technical specifications as per **Annexure-A**.
- d) A copy of this NIT fully signed and stamped in acceptance to all terms and conditions.

Technical bids which are not confirming to the technical specifications will be disqualified.

3) Price Bid / BOQ (Part-B):

The bidder is required to quote his unconditional rates, all inclusive upto IUAC, excluding GST. IUAC is exempted from Custom Duties and GST. Appropriate exception certificates will be

issued. Bidders are advised to quote all items in the BOQ. Incomplete quotes will be rejected.

4) Validity of Tender:

Tender shall be valid for our acceptance without any change in the rates and NIT conditions for a period of 90 days from the date of opening the price bid. No escalation of cost will be acceptable in any condition after opening of tender.

5) Escalation / Deviation:

No escalation or deviation shall be allowed till execution of order / contract.

6) Terms of Payment:

Payment will be released on complete delivery of goods at IUAC and its certification.

7) Delivery Period:

90 days from the date of placement of LOI/Purchase Order. Any delay in completing the supply for reasons attributable to the contractor is liable for liquidated damages as per clause 10 of NIT.

8) Correspondence:

All the correspondence in respect of tender / contractual obligation shall be made to "The administrative Officer (S&P), Inter University Accelerator Centre, Aruna Asaf Ali Marg, New Delhi – 110067". E-mail: joseph@iuac.res.in, Phone: +91-11-24126018, 24126022

9) Tender Rejection:

- (a) IUAC reserves the right to accept/reject any/all tenders in part/full without assigning any reason whatsoever and the decision of the IUAC in this regard will be binding on all the bidders.
- (b) Bids received by means other than e - procurement portal will be rejected.
- (c) Bids not covering full scope of work/supply of the products/goods will be rejected and only complete bids will be considered.
- (d) If BOQ file is found to be modified by the bidder, the bid will be rejected.
- (e) Tenders not complying with any of the provisions stated in this tender document are liable to be rejected.

10) Liquidated damages: In case supply delayed beyond the specified completion period for reasons attributable to the supplier, deductions on account of Liquidated Damages @ 1% of the supply value per week or part thereof will be deducted subject to a maximum of 10% of the total cost.

11) IUAC reserves the right to reject or accept any or all the tenders in full or in part without assigning any reasons whatsoever, and the decision of the Centre in this regard will be binding on all the bidders. Tenders not complying with any of the provisions stated in this tender document are liable to be rejected. **Director, IUAC reserves the right to accept or reject any tender without assigning any reason and does not bind himself to accept the lowest tender.**

12) The contract shall be governed by the Indian Laws. Any dispute arising out of this contract will be subjected to jurisdiction of New Delhi / Delhi.

Accepted
(Signature of Bidder)

Annexure - A

Details of require items

SI.No	Part Descriptions	Part No	Make	Qty
1	RF Power LDMOS Transistor, 1800W(CW), 65Vdc, 1.8MHz-400MHz. Package : NI-1230H-4S Only	MRFX1K80HR5	NXP Semiconductors	25
2	RF Power LDMOS Transistor, 1250W(CW), 50Vdc, 1.8MHz-600MHz. Package : NI-1230H-4S Only	MRFE6VP61K25HR5	NXP Semiconductors	25

Name & Signature of Tenderer
Company with Seal

Annexure - B

(Price should be quoted in the standard BOQ format of this tender)

BOQ / PRICE BID FORMAT

Tender Inviting Authority: INTER UNIVERSITY ACCELERATOR CENTRE

Name of Work/Supply : **Supply of NXP Semiconductors make
RF power LDMOS transistors as per the Annexure-A**

S.No	Part Description	Quantity Required	Unit
1	RF Power LDMOS Transistor, 1800W(CW), 65Vdc, 1.8MHz-400MHz. Package: NI-1230H-4S Part No: MRFX1K80HR5 Make: NXP Semiconductors	25	Nos
2	RF Power LDMOS Transistor, 1250W(CW), 50Vdc, 1.8MHz-600MHz. Package: NI-1230H-4S Part No: MRFE6VP61K25HR5 Make: NXP Semiconductors	25	Nos

Name & Signature of Tenderer

Company with Seal